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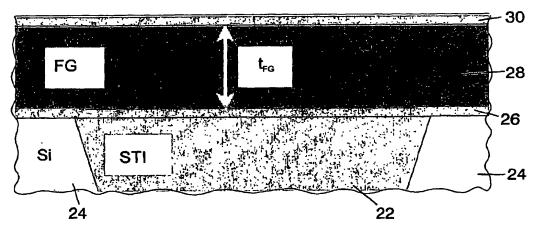
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(54) Title: CONDUCTIVE SPACERS EXTENDED FLOATING GATES



(57) Abstract: A method for manufacturing on a substrate (24) a semiconductor device with improved floating-gate to control-gate coupling ratio is described. The method comprises the steps of first forming an isolation zone (22) in the substrate (24), thereafter forming the floating gate (28) on the substrate (24), thereafter extending the floating gate (28) using polysilicon spacers (40), and thereafter forming the control gate (44) over the floating gate (28) and the polysilicon spacers (40). Such a semiconductor device may be used in flash memory cells or EEPROMs.



CLASSIFICATION OF SUBJECT MATTER PC 7 H01L21/28 H01L H01L21/8247 H01L27/115 H01L29/788 IPC 7 According to International Patent Classification (IPC) or to both national classification and IPC B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) IPC 7 H01L Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Electronic data base consulted during the international search (name of data base and, where practical, search terms used) EPO-Internal C. DOCUMENTS CONSIDERED TO BE RELEVANT Relevant to claim No. Citation of document, with Indication, where appropriate, of the relevant passages 1 - 18DE 198 40 984 A (SIEMENS AG) 18 November 1999 (1999-11-18) the whole document 1 - 18US 6 235 589 B1 (MEGURO HISATAKA) Α 22 May 2001 (2001-05-22) the whole document 1 - 18WO 99 44239 A (HOFMANN FRANZ ; SIEMENS AG Α (DE); WILLER JOSEF (DE)) 2 September 1999 (1999-09-02) the whole document 1-18 US 6 159 801 A (HSIEH CHIA-TA Α 12 December 2000 (2000-12-12) abstract **- Palent family-members-are listed in annex. Further-documents are listed in the continuation of box G. Special categories of cited documents: "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the *A* document defining the general state of the art which is not considered to be of particular relevance invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "E" earlier document but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the act. O document referring to an oral disclosure, use, exhibition or other means document published prior to the international filing date but later than the priority date claimed "&" document member of the same patent family Date of mailing of the international search report Date of the actual completion of the international search 29/10/2003 22 October 2003 Authorized officer Name and mailing address of the ISA

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